

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

Sheet	1	of	2
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Application Number	10/674,835
Filing Date	September 29, 2003
First Named Inventor	Jose S. Niell
Art Unit	2129
Examiner Name	
Attorney Docket Number	42P17022

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Based on PTO/SB/08A (08-03) as modified by Blakey, Solokoff, Taylor & Zafran (w/r) 08/11/2003.

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Substitute for form 1449A/PTO				Complete if Known	
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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.†	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-Issue number(s), publisher, city and/or country where published.		T²
OFR		HNATEK, EUGENE R., "A User's Handbook of Semiconductor Memories", 1977, A Wiley-Interscience Publication, John Wiley & Sons, Inc., Pgs. 163-181		
OFR		TAUB, HERBERT et al., "Digital Integrated Electronics", 1977, McGraw-Hill Book Company, Pgs. 394-396		
OFR		MANO, M. MORRIS, "Computer System Architecture, Second Edition", 1982, Prentice-Hall, Inc., Pgs. 426-428		
OFR		HOROTITZ, PAUL et al., "The Art of Electronics, Second Edition", 1989, Cambridge University Press, Pg. 541		
OFR		"CMOS BiCMOS Data Book", Cypress Semiconductor Corporation, February 1, 1989, Pgs. 5-34 through 5-57		
OFR		"Databook Data Converters - DSP Products", TRW LSI Products Inc., 1990, Pgs. 23-52		
OFR		DORF, RICHARD C., "The Electrical Engineering Handbook", 1993, CRC Press, Inc., Pgs. 1725-1727		

Examiner Signature	/Omar Fernandez Rivas/	Date Considered	12/8/2006
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